

【図3】

Fig. 3A

(a)

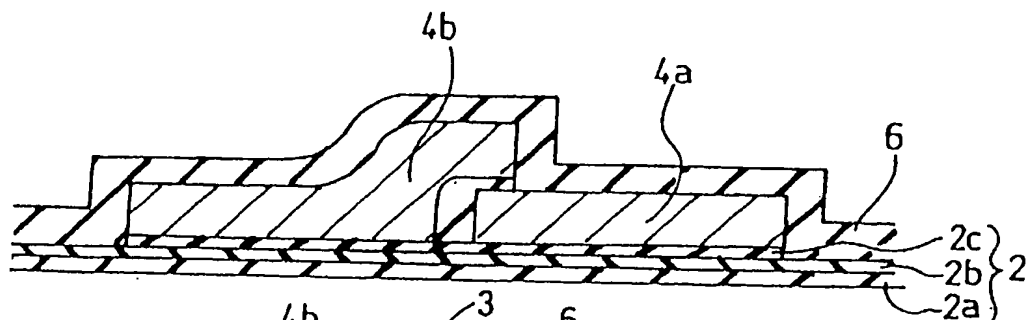


Fig. 3B

(b)

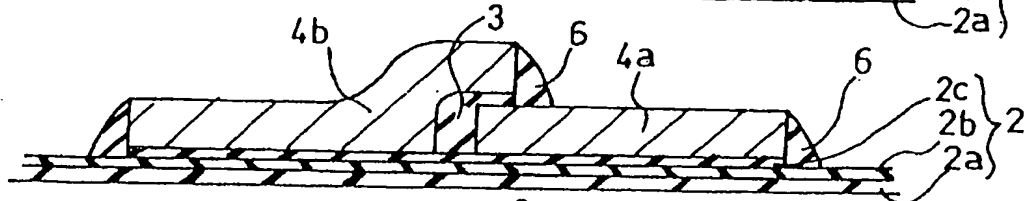


Fig. 3C

(c)

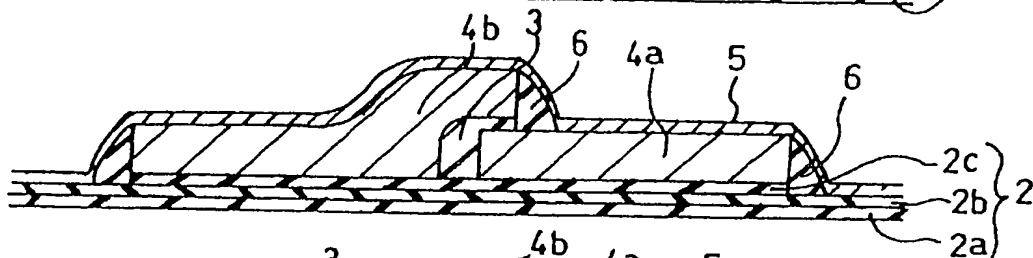


Fig. 3D

(d)

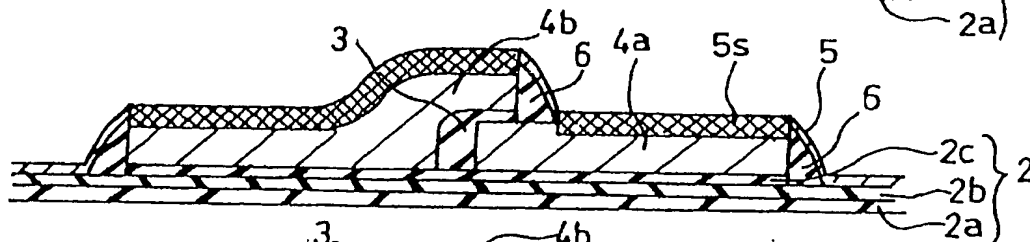
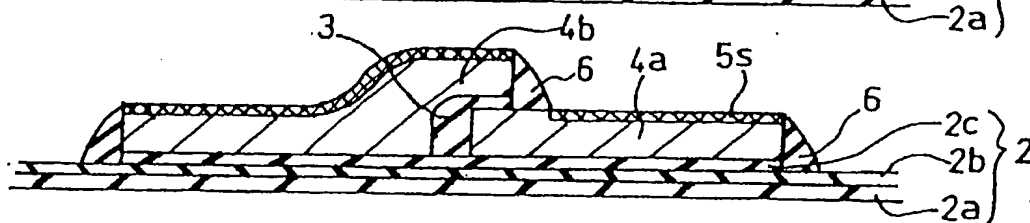


Fig. 3E

(e)



{図4}-

Fig. 4A
(a)

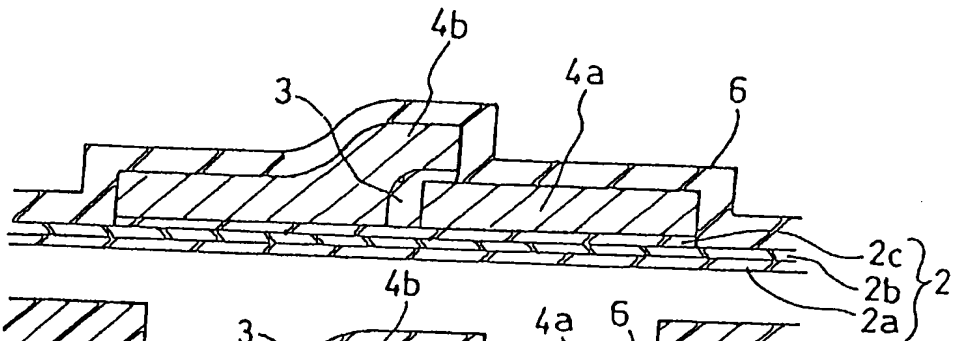


Fig. 4B
(b)

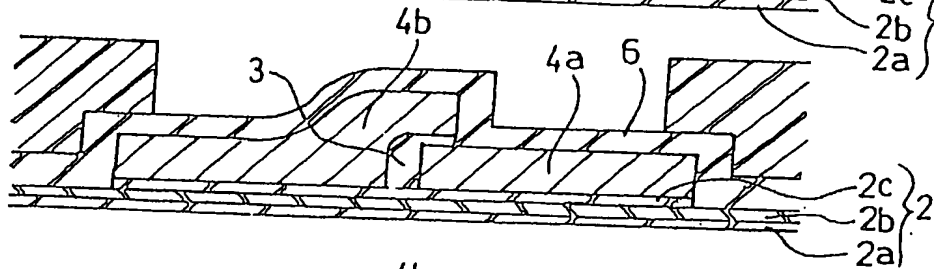


Fig. 4C
(c)

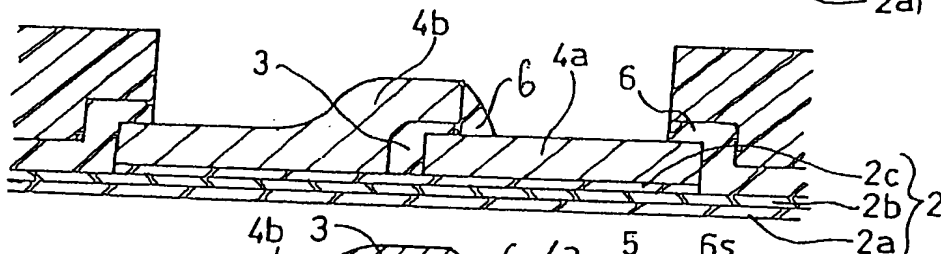


Fig. 4D
(d)

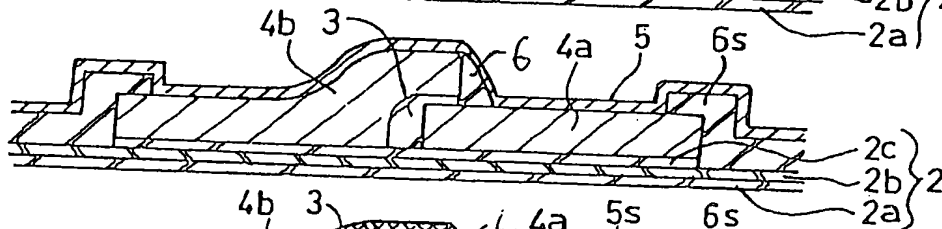


Fig. 4E
(e)

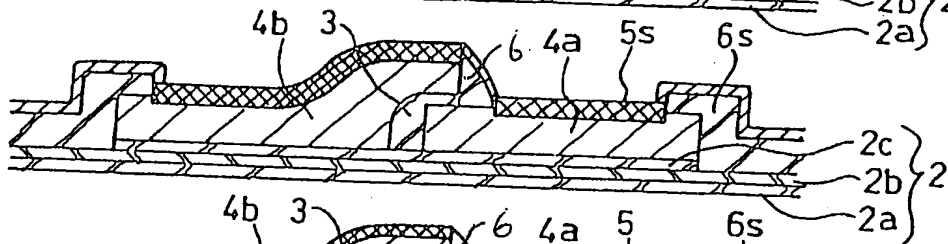
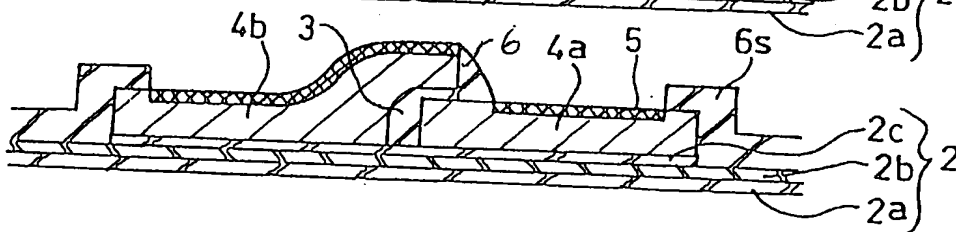


Fig. 4F
(f)



【図5】

Fig. 5

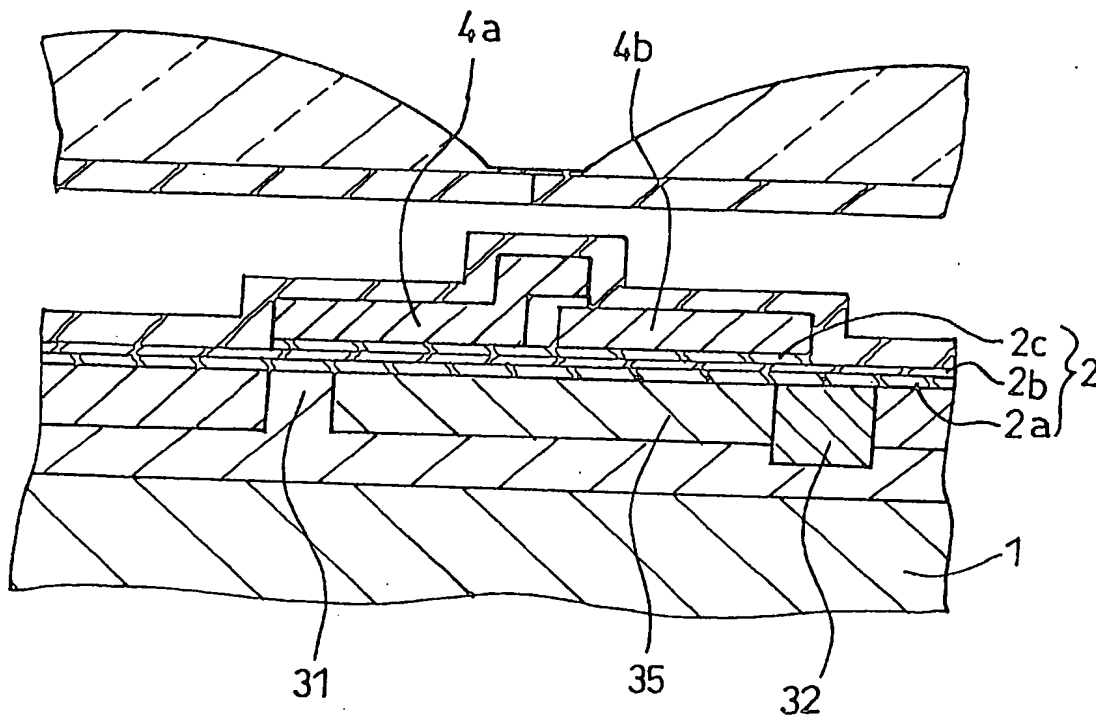
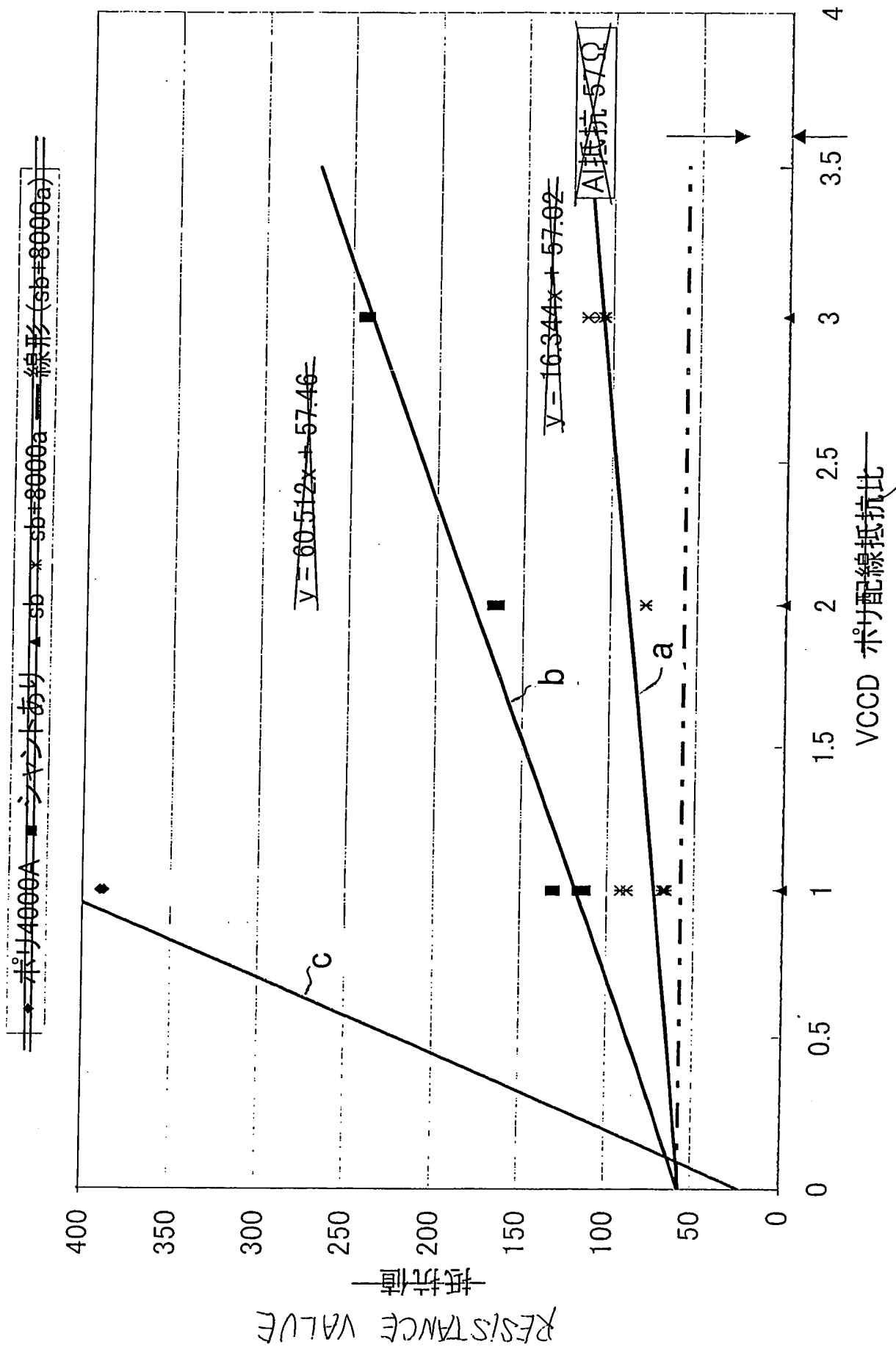


Fig. 6



WIRING RESISTANCE RATIO OF
POLYCRYSTALLINE SILICON

【図7】

Fig.7A (a)

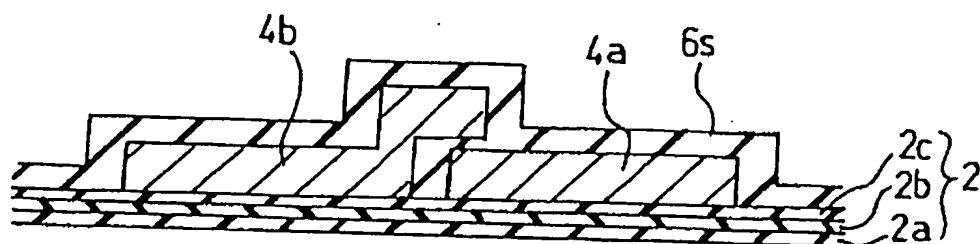


Fig.7B (b)

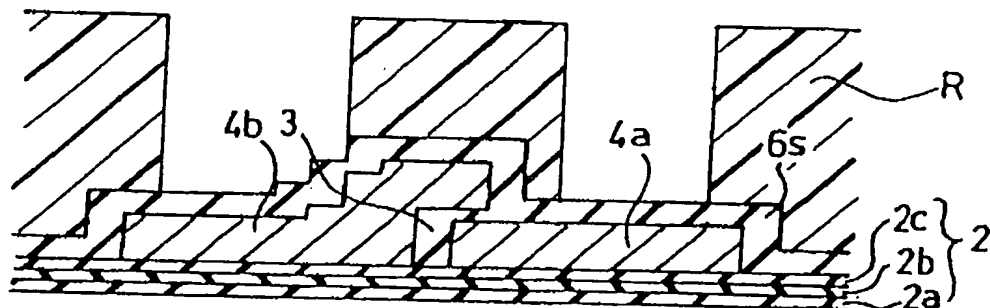


Fig.7C (c)

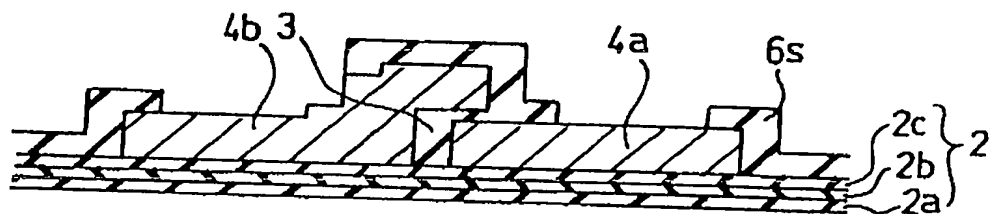


Fig.7D (d)

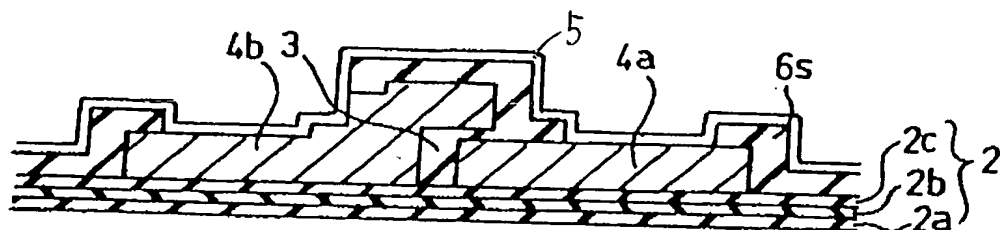


Fig.7E (e)

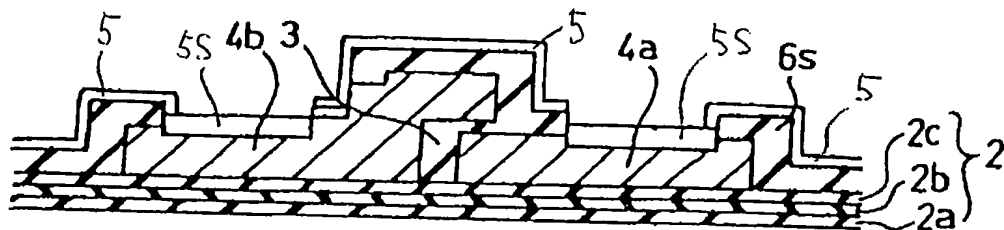


Fig.7F (f)

